**DAILY ASSESSMENT FORMAT**

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| **Date:** | **28 MAY 2020** | **Name:** | **HARSHITHA H** |
| **Course:** | **LOGIC DESIGN** | **USN:** | **4AL18EC020** |
| **Topic:** | **1.Analysis of clocked sequential circuits**  **2.Digital clock design** | **Semester & Section:** | **IV SEM & A SECTION** |
| **Github Repository:** | **harshithah** |  |  |

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| **FORENOON SESSION DETAILS** | | | |
| **Image of session**  **C:\Users\harshitha\Pictures\Screenshots\Screenshot (83).png**  **C:\Users\harshitha\Pictures\Screenshots\Screenshot (84).png**  **C:\Users\harshitha\Pictures\Screenshots\Screenshot (86).png**  **Report –**  **Day 2: LOGIC CIRCUITS**  **Analysis of clocked sequential circuits:**   * **Positive edge triggered D flip-flop** * **Analysis with D flip-flop** * **Analysis with JK flip-flop** * **Analysis with T flip-flop** * **State reduction and assignment** | | | |
| **STIMULATED CIRCUITS USING LOGISIM:**  **C:\Users\harshitha\Pictures\Screenshots\Screenshot (91).png** | | | |
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| **Date:28 MAY 2020** |  | **Name:HARSHITHA H** |  |
| **Course: PYTHON** |  | **USN: 4AL18EC020** |  |
| **Topic:1. Object Oriented Programming** |  | **Semester & Section: IV SEM & A SECTION** |  |
| **AFTERNOON SESSION DETAILS** | | | |
| **Image of session**  **C:\Users\harshitha\Pictures\Screenshots\Screenshot (87).png**  **C:\Users\harshitha\Pictures\Screenshots\Screenshot (89).png**  **C:\Users\harshitha\Pictures\Screenshots\Screenshot (90).png** | | | |
| **Report –**  **PYTHON:**  **OBJECT ORIENTED PROGRAMMING:**   * **Turning application into OOP style** * **Creating bank account** * **Inheritance** * **OOP glossary** * **GUI in oop design** | | | |